



Title: SEMICONDUCTOR INTEGRATED  
CIRCUIT AND LEVEL CONVERSION  
CIRCUIT

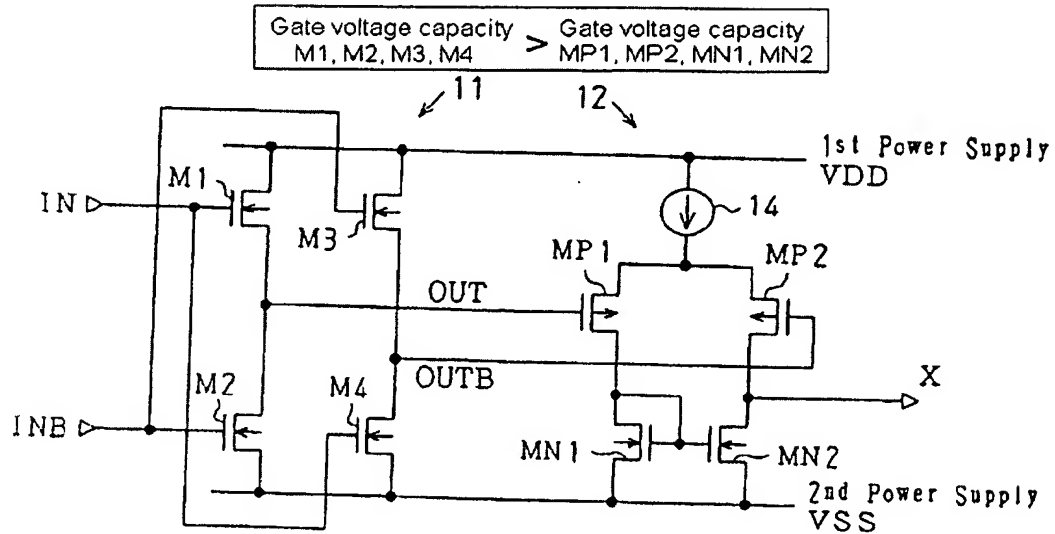
Inventor: Tomohiko KOTO

Appl. No.: 10/609,894

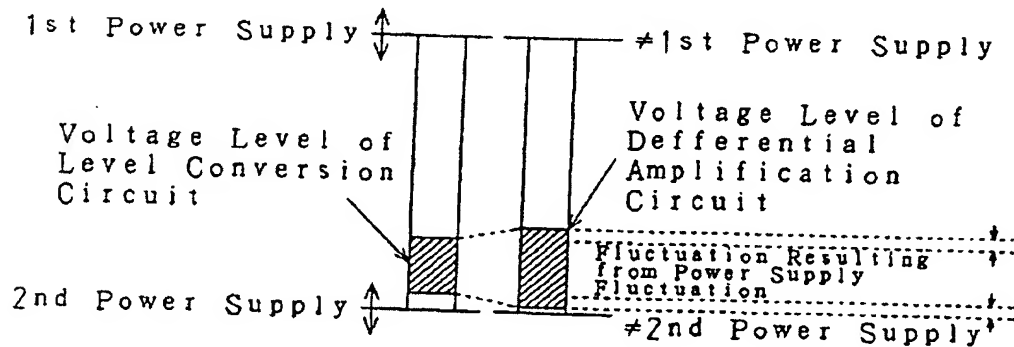
Docket No.: 108075-00126

## REPLACEMENT SHEET

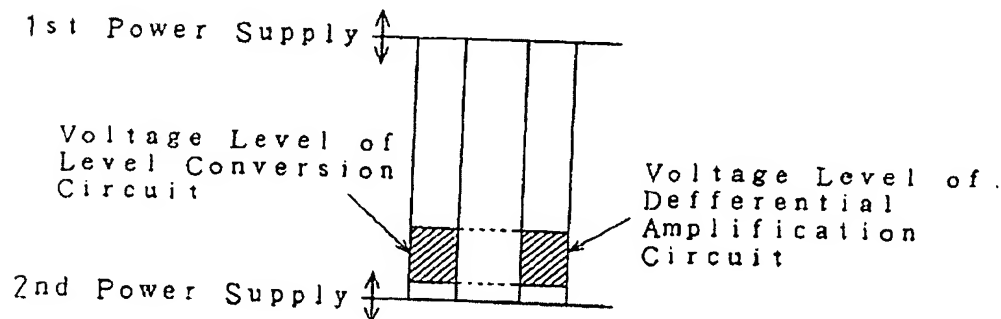
### Fig. 5



### Fig. 6A



### Fig. 6B



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New Sheet

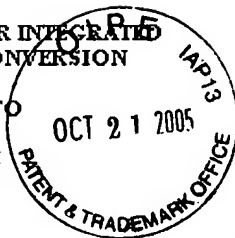


Fig. 15

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Gate Oxide Film transistors  
M1, M2, M3, M4



Gate Oxide Film transistors  
MP1, MP2, MN1, MN2

